



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.	09/936,032	Confirmation No.	9437
First Inventor	Michael Nicolaidis	Filing Date	March 11, 2002
Tech. Center/ Art Unit	2133	Examiner	Gandhi, Dipakkumar B.
Title:	Logic Circuit Protected Against Transient Disturbances		
Docket No.:	IRO003 US	Customer No.:	34036

Santa Clara, California
August 1, 2005

Mail Stop AMENDMENT
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Dear Sir:

In response to Office Action dated May 2, 2005, please amend the above-identified application as follows.

Amendments to the Specification are described on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks/Arguments begin on page 6 of this paper.

Amendments to the Specification:

Please add the abstract which is attached at the end of this amendment,
as a separate page.